

**IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE**

Applicants: Taylor et al.	)	I hereby certify that this paper is
	)	being electronically deposited with
Serial No.: 10/829,495	)	the United States Patent and
	)	Trademark Office on this date:
Filed: April 22, 2004	)	
	)	
For: "Method and System for Fail-Safe	)	<b>September 29, 2009</b>
Renaming of Logical Circuit Identifiers for	)	
Rerouted Logical Circuits in a Data	)	
Network"	)	
	)	<u>/Felipe Hernandez/</u>
Group Art Unit: 2419	)	Felipe Hernandez
	)	Registration No. 61,971
Examiner: Ashley L. Shivers	)	Attorney for Applicants

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The documents listed on the attached PTO/SB/08a are submitted pursuant to 37 CFR  
§§ 1.56, 1.97, and 1.98.

### **TIME OF FILING**

This information disclosure statement is being filed before the mailing of a first Office action after the filing of a request for continued examination under § 1.114. In accordance with 37 CFR §1.97(b)(4), no certification or fee is required.

### **METHOD OF PAYMENT**

The Applicants do not believe that a fee is required for this request. However, the Commissioner is hereby authorized to charge any fees which may be required to Deposit Account No. 50-2455.

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Respectfully submitted,

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**September 29, 2009**